

PTO/SB/08A (08-00) Approved for use through 10/31/2002, OMB 0651-0031

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Sheet	1	of	2	Attorney Docket Number	LVPAT026US	78	; =

				U.S. PATENT DOCU	MENTS	
Examiner Initials*	Cite No.1	U.S. Patent I	Cocument Kind Code ² (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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				FORE	IGN PATENT DOCUMENT	S		
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¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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				Application Number				
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		OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS	•
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T2
ton		STEVEN F. OAKLAND, "Considerations for Implementing IEEE 1149.1 On System-on-a-Chip Integrated Circuits", International Test Conference 2000 Proceedings, October 3-5, 2000, p. 628-637	
JOM		LEE WHETSEL, "An IEEE Based Test Access Architecture for ICs With Embedded Cores", International Test Conference 1997 Proceedings, November 1-6, 1997	
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Sheet 1 of 2

Complete if Known						
Application Number	09/843,307					
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First Named Inventor	NADEAU-DOSTIE					
Group Art Unit	2816					
Examiner Name	N/A					
Attorney Docket Number	LVPAT026US					

				U.S. PATENT DOCU	MENTS	
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Jb/		5,884,023		Swaboda et al.	03-16-1999	
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				FORE	IGN PATENT DOCUMENTS	S		
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Initials	No.1	Office ³	Number ⁴	Kind Code ⁵ (il known)	Applicant of Cited Document	Cited Document MM-DD-YYYY	Passages or Relevant Figures Appear	T ⁶
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	OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS							
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Ton		ALVES, G.R. et al., Using the BS Register for Capturing and Storing n-bit Sequences in Real Time, IEEE Proceeding of the European Test Workshop. May 1999. pages 130-135.						
20m		HAMILTON, C. et al., Methods for Boundary Scan Access of Built-In Self-Test for Field Programmable Gate Arrays. IEEE Proceedings of Southeastcon. March 1999. pages 69-78.						

Examiner Signature	Joseph :	\mathcal{D}	Musky	Date Considered	8/18/04	

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